

# Discrete Event Systems

## Exercise Sheet 11

### Change log:

- 30.11.2023: fixed the NOR-gate caption.

A complex system is often designed in a hierarchical fashion. That is, different components are designed independently and then assembled into the overall system. For example, a car manufacturer assembles different parts that are produced by specialized suppliers. The combined behavior of the assembled parts results in the overall system behavior.

Some combined behaviors are allowed (e.g., you can listen to the radio while driving), whereas some others must be prevented (e.g., the engine must not start if the gas tank is open). Therefore, the interaction between components needs to be controlled, which is the goal of the *system specification and verification*.

In this exercise session, we look at these two steps. In the first exercise, we use set representations and characteristic functions to express system properties. In the second exercise, we represent the system design and specification using Boolean expressions, and we use *binary decision diagrams* (BDDs) as a means to verify that the design satisfies the specification.

## 1 Set Representation

### 1.1 Set operations and characteristic functions

In the early design phase of a system, it is common to identify some states as *faulty*, or *error* states. One goal is to ensure that, the system never enters such states. We define the following sets of states:

- $X$ : the set of *all* states.
- $N$ : the set of *normal* states.
- $E$ : the set of *error* states.
- $O$ : the set of states where *there is a memory overflow*.

**Definition 1** For a given state  $x$  and a set of states  $Q$ , the characteristic function of  $Q$  is defined as following

$$\psi_Q(\sigma(x)) = \begin{cases} 0, & x \notin Q \\ 1, & x \in Q \end{cases}, \quad (1)$$

where  $\sigma(x)$  is the binary encoding of the state  $x$ .

In each of the following sub-questions, we are interested in describing the property of the given sets of states using set operations and characteristic functions.

- a) What is  $\psi_X$ , the characteristic function of *the set of all states*?
- b) "Each state (in the set of all states) is either a normal or an error state or both". Express this property in terms of sets and characteristic functions.
- c) "If a state (in the set of all states) is in the set of overflow states, it is not a normal state". Express this property in terms of sets and characteristic functions.
- d) Describe  $Q_1$ , the set of *error states* which are not an overflow, in terms of sets and characteristic functions.
- e) Describe  $Q_2$ , the set of states that satisfies  $O \Rightarrow E$ , i.e., the set of states for which this property holds, in terms of sets and characteristic functions.  
*Hint:*  $O \Rightarrow E$  reads  $O$  implies  $E$ , in other words, if a state is in  $O$ , then it is in  $E$ . Beware that we look for the states for which this property holds true, not a relation between  $O$  and  $E$ .

## 1.2 Write specifications in Boolean encoding and compose them

Consider a sensor network composed of 3 *sensor nodes*, a *bus* and a *sink* (i.e., a node where data is collected). The network can be in *normal mode* or *bootstrapping mode*, a node can be in *sleep mode* or in the *awake mode*. In order to save energy, nodes are put in sleep mode whenever possible.

Below is a set of constraints described in plain texts, and a set of Boolean variables that encode the system states. Your task consists in (1) expressing each of the constraints individually, and (2) combining them to express the overall system specification.

The list of constraints:

- **C1:** When *one or more* nodes are using the bus, the sink must be awake to receive data.
- **C2:** *No more than one* node can use the bus at the same time.
- **C3:** When the network is in bootstrapping mode, then the sink must be awake, and the nodes cannot use the bus.

The encoding:

- $x_1 = 1$ : Node 1 is using the bus.
  - $x_2 = 1$ : Node 2 is using the bus.
  - $x_3 = 1$ : Node 3 is using the bus.
  - $x_s = 1$ : The sink is in awake mode.
  - $x_b = 1$ : The network is in bootstrapping mode.
- a) Express the specification of **C1**, **C2** and **C3** using the given Boolean encoding.
  - b) What is the specification of the desired behavior? **Hint:** All constraints should be satisfied.

## 2 Binary Decision Diagrams

For a *reduced ordered binary decision diagram* (ROBDD) representing a Boolean function with  $N$  binary variables, we denote the variable order by  $\Pi : x_1 \prec x_2 \prec \dots \prec x_n$ , where  $x_1$  is the highest variable of the tree,  $x_2$  the second highest, and so on. The notation  $x_i \prec x_j$  is used to indicate that we evaluate  $x_i$  before  $x_j$ . An ordering  $\Pi_1$  is said to be better than  $\Pi_2$  for an ROBDD  $G$ , if  $G$  contains fewer decision nodes when using  $\Pi_1$  rather than  $\Pi_2$  (after merging equivalent nodes).

In the following, use the following notation to represent ROBDDs: use a solid arc (——) to indicate that the parent node evaluates to 1, and use a dashed arc (---) otherwise. **Do**

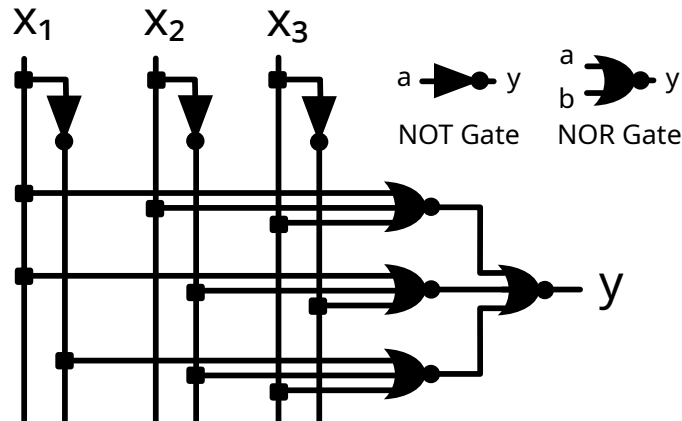
**not** use color to differentiate the type of nodes/edges (not allowed to use multiple colors in the exam).

## 2.1 Verifying the Equivalence of Combinational Circuits Using BDDs

You are in the process of designing a processing architecture. Your specification analysis results in the following desired function to implement:

$$F_1 := x_1\bar{x}_2 + x_1x_3 + \bar{x}_2x_3 + \bar{x}_1x_2\bar{x}_3. \quad (2)$$

For practical reason, you only allowed to use **NOT** gates and **NOR** gates to implement your circuit. Considering this constraint, your synthesis tool returns the following schematic:



Your team leader is quite old-fashioned and does not trust these new fancy software so much (or maybe he/she is just testing you!). He/She asks that you verify the schematic circuit does indeed implement the same function as  $F_1$ . This can be done efficiently using ROBDDs.

- Express the function  $F_2$  realized by the circuit.
- Draw and compare the ROBDDs of  $F_1$  and  $F_2$  using the ordering of variables  $\Pi : x_1 \prec x_2 \prec x_3$ . Do they implement the same behavior?

## 2.2 ROBDDs with respect to different orderings

- Consider a Boolean function  $G(x_1, x_2, y_1, y_2) := (x_1 \leftrightarrow y_1) \cdot (x_2 \leftrightarrow y_2)$ , where  $(x \leftrightarrow y)$  denotes  $x \cdot y + \bar{x} \cdot \bar{y}$ . Write the Boole-Shannon decomposition of  $G$  with respect to  $\Pi$ .
- Consider the ordering of variables  $\Pi : x_1 \prec x_2 \prec y_1 \prec y_2$ , draw the corresponding ROBDD for  $G$ .
- Consider a new ordering  $\Pi' : x_1 \prec y_1 \prec x_2 \prec y_2$ . Use it to reconstruct the ROBDD of  $G$ . Is  $\Pi'$  a better ordering than  $\Pi$  for  $G$ ?